

REMARKS

Claims 1-3 and 5-23 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

By separate letter, a proposed drawing correction to Figures 2, 4, 6, 10, and 12 has been submitted for consideration of the Examiner.

The Examiner alleges that DAC must be shown.

The Examiner's attention is directed to Figure 4 element 50. Here, the DAC is shown.

Is respectfully submitted that these proposed drawing corrections and arguments obviates the objection to the drawings.

Claims 5 and 9 were objected to because of informalities.

Claim 5 has been amended to eliminate the informalities.

It is respectfully submitted that Claims 1-3 and 5-23 are now free from informalities.

Claims 2, 3, 18, 19, and 20 were rejected under 35 U.S.C. §112, second paragraph as being indefinite.

These rejections are respectfully traversed in part.

The Examiner questions the charge mode and the voltage mode.

The Examiner's attention is directed to page 9, line 9 et seq. where these modes are explained.

With respect to Claim 18, the Examiner's attention is directed to Claim 17 where the first output is described.

Claims 1-3 and 5-23 are in full compliance with 35 U.S.C. §112.

Turning now to the art rejections, Claims 1-3 and 5-19 were rejected under 35 U.S.C. §103(a) as being unpatentable over Wilson in view of Fontanella; and Claims 20-23 were rejected under 35 U.S.C. §103(a) as being unpatentable over Wilson in view of Kondou.

These rejections are respectfully traversed.

It is respectfully submitted that Wilson does not disclose or suggest the presently claimed invention including a piezo actuator drive circuit to drive a disk as defined in independent Claim 1.

Wilson is directed to an ultrasonic generator and consequently could not disclose or suggest the presently claimed invention.

It is respectfully submitted that Fontanella does not disclose or suggest the presently claimed invention including a drive amplifier adapted to drive a piezo actuator in a voltage mode and in a charge mode as defined in independent Claim 1.

Fontanella discloses a driver circuit for controlling a piezo actuator in a charge mode. However, nothing in Fontanella discloses a charge circuit to drive a piezo actuator in both and voltage mode and a charge mode.

Whether or not Kondou discloses a DC control circuit, a compensation loop, a DAC, and an ADC and whether one of ordinary skill in the art would consider modifying Wilson is of no moment since the result in construction would in no way disclose or suggest the presently claimed invention.

It is respectfully submitted that Claims 1-3, and 5-23 patentably define over the applied art.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE."

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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VERSION WITH MARKINGS TO SHOW CHANGES MADE

Claims 5 and 22 have been amended as follows:

5. The drive circuit as specified in Claim 1 -4- wherein the sensing circuit provides a signal indicative of the piezo actuator position.
22. The drive circuit as specified in Claim 1 further comprising a digital-to-analog (DAC) coupled to an input of said drive amplifier and a voltage reference being coupled to another input of said drive amplifier.

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